

The rejection of claims 1 and 2 states in part:

Regarding claim 1, Yasooka teaches a receiver comprising RF stage 12 (fig. 1) for receiving an antenna signal from an inductive antenna 1, a processing stage 4 for processing the output signals of the RF stage and an output for supplying an audio signal 8 (fig. 1), characterized in that the RF stage comprises *electronically* switched capacitors 12a, 12b, controlled by a *switch control circuit* 3 for adjusting front end selectivity of the RF stage to correspond to an established tuning frequency ([0011], [0017]). (Italics added.)

Applicant respectfully disagrees. There is no switching of capacitors under electronic control in Yasooka. Rather, the switch 12c is, from all indications, manually switched. Furthermore, the "frequency changing circuit" 3 of Yasooka is simply a well-known downconverter, i.e., RF mixer. It does not in any way affect the capacitors 12a and 12b. Note the description on the bottom third of page 2 of the translation of Yasooka, which describes the received frequency f_1 being downconverted to an intermediate frequency f_3 using a local oscillator signal of frequency f_2 , where $f_3=f_2-f_1$.

As the interpretation of the reference as applied to the claims is in error, claim 1 as amended is believed to patentably distinguish over the cited references. Claims 2 and 3 are believed to add novel and patentable subject matter to independent claim 1. Withdrawal of the rejections and allowance of claims 1-3 is respectfully requested.

Respectfully submitted,



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Dated: July 19, 2004